

Notice of References Cited	Application/Control No. 10/588,191		Applicant(s)/Patent Under Reexamination KINOSHITA ET AL.	
	Examiner BRANDON S. COLE		Art Unit 4125	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-6,891,433	05-2005	Schrader, Victor Paul	330/253
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	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Design and Analysis of Integrator-Based Log-Domain Filter Circuits by Gordon W. Roberts, Vincent W. Leung http://books.google.com/books?id=g4A4yTP-TZ4C&pg=PA2&lpg=PA2&dq=transistor+acting+as+resistor&source=web&ots=rM3nE3NmCA&sig=hkD0mF3pX8NWFsBtQbw1ptGL0fQ#PPA3,M1
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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